NEWS RELEASE

SANDIA NATIONAL LABORATORIES LICENSES FLEX LOGIX'S EMBEDDED FPGA IP FOR MULTIPLE PRODUCTS

Embedded FPGA Revolutionizes Chip Development by Enabling Organizations such as Sandia to Reconfigure RTL at any Point During the Design Process

Mountain View, Calif., October 10, 2017 – Addressing the growing industry need to use embedded FPGA technology to design chips and MCUs, Flex Logix™ Technologies, Inc., announced today that Sandia National Laboratories has licensed its EFLX reconfigurable logic IP. As part of their licensing agreement, Flex Logix has delivered the Gen 2 EFLX-2.5K logic core in Sandia's proprietary 180nm process for the development of multiple Sandia chip products for use in Sandia projects.

"Embedded FPGA technology is a game changer in the chip design process and we are pleased to add Sandia to our growing customer base," said Geoff Tate, CEO and co-founder of Flex Logix. "We expect our EFLX platform to become widely used in the industry given that chip development costs and lead times keep increasing. The ability to reconfigure RTL at any time can avoid expensive chip spins, enable one chip to address many customers/applications, and can extend the life of chips and the systems they are in by enabling them to keep up with changing standards and customer requirements."

The EFLX-2.5K Logic IP core has 2520 LUTs, 632 inputs and 632 outputs and is a complete embedded FPGA. The EFLX-2.5K core can be tiled to make larger arrays to support applications needing more LUTs as required.

EFLX is available in two core sizes (-100 and -2.5K) today on multiple mainstream foundry processes: 40nm, 28nm and 16nm. As this announcement indicates, EFLX can also be ported to any CMOS process whether it be at a commercial foundry or organizations with their own fabs. EFLX is a digital architecture for development of embedded FPGAs for integration into SoCs, ASICs and MCUs of a wide range of sizes. The EFLX arrays are programmed using VHDL or Verilog; the EFLX compiler takes the output of a synthesis tool such as Synopsys Synplify and does packing, placement, routing, timing and bitstream generation. The bitstream when loaded into the array programs it to execute the desired RTL.

About Sandia National Laboratories

For more than 60 years, Sandia National Laboratories has been the premier science and engineering laboratory in the United States for national security and innovation. Working closely with U.S. government agencies, private industry and academic institutions, Sandia has led the

charge to research, develop and deliver essential technologies used to solve many of the nation's most important security, climate change and sustainable energy challenges.

About Flex Logix

Flex Logix, founded in March 2014, provides solutions for reconfigurable RTL in chip and system designs using embedded FPGA IP cores and software. The company's technology platform delivers significant customer benefits by dramatically reducing design and manufacturing risks, accelerating technology roadmaps, and bringing greater flexibility to customers' hardware. Flex Logix has secured approximately $13 million of venture backed capital, is headquartered in Mountain View, California and has sales rep offices in China, Europe, Israel, Japan, Taiwan and Texas. More information can be obtained at http://www.flex-logix.com or follow on Twitter at @efpga.

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