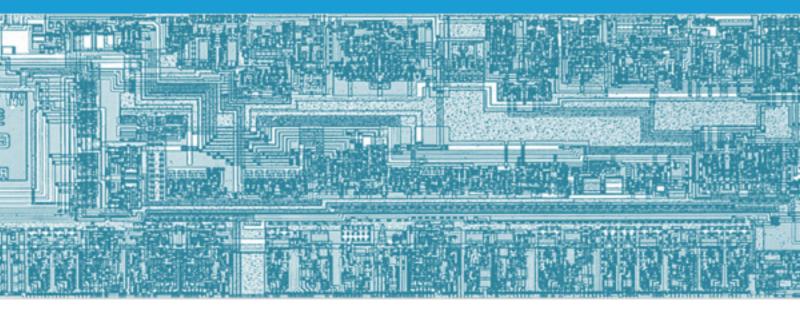


A SILICON CATALYST NEWSLETTER

A VALUABLE RESOURCE FOR THE SEMICONDUCTOR STARTUP COMMUNITY



VOLUME 1 JUNE 2017





RICK LAZANSKY

ceo - silicon catalyst serial entrepreneur and incubator fanatic

MELCOME MARIAN

The Silicon Catalyst team welcomes everyone to the first edition of the Silicon Startup Solutions newsletter. Our goal is to publish these quarterly, in the hope that it will strengthen the ecosystem we're building together, and through that ecosystem, rebuild and strengthen the world of semiconductor startups, the likes of which created Silicon Valley. We'll invite our startups, as well as industry veterans and visionaries, to author short articles we believe you will find of interest.

Silicon Catalyst is the world's only startup incubator focused exclusively on semiconductor solutions. We are a startup ourselves and are the recipient of the prestigious UBM / Canon ACE (Annual Creativity in Electronics) Award for 2015 Startup Company of the Year. Like most startups, we're small, lean, and nimble. We spend valuable time debating priorities. both internal and external, about our scope, mission and operation. Our team is comprised of seasoned Silicon Valley veterans with a lifetime of semiconductor experience who recognize the enormous value and impact of startups. Driven by passion, we have a mission, and we have energy, enthusiasm, and are dedicated to improving the world-wide environment for starting really high tech companies based. at least in part, on our semiconductor foundation. We're keen on improving the business conditions for startups to thrive. We do this by providing goods and services that would otherwise cost investors tens of millions of dollars; by providing personal advisors in addition to connecting startups with our strategic partners; helping companies ready themselves for funding, finding and supporting their efforts in the capital search; and showcasing themselves at unboxing events and demo days.

There has been a very strong headwind for the past decade for startups in our industry. That's changing. The number of startups is on an upswing. We've received well over a hundred applications to Silicon Catalyst since our launch event in April of 2015. A good rule of thumb in the VC community is for every application, there are ten more companies not yet visible. That bodes well for the near future.

I recommend everyone read a wonderful report which came out in January –

"Report to the President, Ensuring Long-Term U.S. Leadership in Semi-conductors" (https://obamawhite-house.archives.gov/sites/default/files/microsites/ostp/PCAST/pcast_ensuring_long-term_us_leadership_in_semiconductors.pdf).

While the report does directly address the need to "push back against innovative-inhibiting Chinese industrial policy", it does so with the idea of promoting transparency, avoiding "distorting the market behavior" and "ensuring a level playing field." Most importantly, it stresses the need to "improve the business environment for U.S. based semiconductor producers", and "help catalyze transformative semiconductor innovation over the next decade".

There's a balance in the report; it stresses the need to respond to the challenge of ever improving innovation and competition. I believe there is great alignment with our goals at Silicon Catalyst. We're not just about U.S. based producers - half of our portfolio is outside the U.S. borders but a large percentage of the economies of all major countries is dependent on the hardware and software. Annual estimates range from 7 to 12 trillion dollars of direct contribution to combined world domestic product. These figures, along with our input and guidance, should encourage all of you reading this newsletter to THINK BIG

WHO WE ARE

Silicon Catalyst is the only incubator focused exclusively on solutions in silicon, building a coalition of in-kind and strategic partners to dramatically reduce the cost and complexity of development.

With a world-class network of mentors to advise startups, Silicon Catalyst is helping startups address challenges moving from idea to realization. The incubator/accelerator supplies startups with facilities, a path to funding, tools, networking and marketing acumen to successfully launch their companies' novel technology solutions. Silicon Catalyst has partnered with the largest companies in the ecosystem providing in-kind services to our startups, giving them access to design tools, silicon shuttle runs, and testing and design services valued at over \$100 million. The concert of these benefits acts to "right-size" the cost of semiconductor innovation and lay the groundwork so companies can bring their technology to market more quickly.

Silicon Catalyst was the recipient of the prestigious 2015 ACE Award for Start-Up Company of the Year.





























\$100+ MILLION OF **IN-KIND SERVICES** Silicon Catalyst has partnered with the

largest companies in the ecosystem to ductor startups: design tools, silicon shuttles, test and design services, etc.

SCREENED

Since our Silicon Sunrise launch event back in April of 2015 we have screened over 100 startups



Though our incubator is located in Silicon Valley, roughly 50% of our startups are located outside of California. Our admitted Portfolio Companies span the globe from

VOLUME 1

IN THIS ISSUE

We hope you enjoy the inaugural edition of Silicon Startup Solutions, a newsletter published by Silicon Catalyst to foster meaningful dialogue within the semiconductor startup community. Please explore the interactive links embedded within this pdf newsletter. We encourage you to take the time to read this on your laptop or a tablet or simply print a copy to keep. We welcome your feedback. Contact Us



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On February 16, Silicon Catalyst held its Q1 startup selection screening meeting at the O'Melveney and Myers (OMM) boardroom on Sand Hill Road in Menlo Park. We are pleased to announce that we admitted of two of those promising startups to Silicon Catalyst's growing list of Portfolio companies-REX Computing and ALP.

Wunderkind REX founder Thomas Sohmers, named a '20 under 20' Peter Thiel scholar, is truly one of the young luminaries on the semiconductor horizon today. REX Computing is rethinking the traditional hardware managed cache hierarchy. In removing unnecessary complexity, they are able to significantly reduce power consumption which delivers a 10 to 25x increase in energy efficiency for the same performance level compared to existing GPU and CPU systems.

ALP founder. Basol Bulent has over 167 issued US patents and over 100 publications on semiconductor devices, thin film processes and semiconductor processing tools. ALP's ALPro Technology has the unique capability of measuring and profiling all of the critical electrical properties (mobility, sheet resistance, active carrier concentration) of semiconductor layers at high resolution (0.2 - 1 nm), rapidly (<1hr). Application areas include precise characterization of source-drain regions and new materials (SiGe, Ge, III-V) used in advanced device structures.

We are also very pleased to introduce <u>Lumerical</u>, and <u>IC Manage</u> as our newest in-kind partners. In the pages that follow, we feature an interview with Silicon Catalyst Portfolio company <u>AEPONYX</u> and a spotlight on in-kind partner <u>Lumerical</u>.





The Silicon Catalyt Q1 screening meeting was held to a packed house at the law offices of OMM. The event was attended by VC's, strategic and in-kind partners, advisors, and Silicon Catalyst team members.

A CONVERSATION WITH PHILIPPE BABIN CEO OF AEPONYX, A SILICON CATALYST PORTFOLIO COMPANY



AEPONYX is a fabless micro optical switch semiconductor start-up, designing and manufacturing chips for fiber optic access to the cloud. With its patented and patent pending technologies, combining planar Micro-Electro-Mechanical-Systems (MEMS) and silicon photonics, AEPONYX is building the fastest, most affordable and smallest micro optical switches for use in disaggregated switching systems and enabling tunability in both transmission & reception in pluggable transceivers.

Q. PLEASE TELL ME ABOUT YOUR STARTUP AND GOALS?

A. AEPONYX aims at being the leader in silicon photonics with MEMS. We are combining planar MEMS with high efficiency silicon nitride waveguides to create silicon photonics applications needed for telecom and datacom applications. The applications cover tunable transceivers in the telecom space as well as optical switches in the data center space.

We are now in the development phase, with first customer prototypes planned before year end and pre-production launch by end of next year.

Q. PLEASE TELL ME ABOUT YOUR SPECIFIC ROLE/TITLE AND BACKGROUND?

A. I am the CEO of AEPONYX as well as the VP of business development. I spent the last 23 years in the telecom space, successively working in electronic manufacturing, telecom product development and now the silicon photonic space. My experience is quite diverse, covering manufacturing, engineering, product development, product management, marketing, business development, sales and general management. I have spent my entire career in small enterprises and start-ups.

Q. WHAT INSPIRED YOU TO START THIS COMPANY?

A. I must admit that I got dragged into it. When AEPONYX was started, the goal was to design systems for service providers. My CTO had developed a WDM-PON solution, based on some genuine components designed in Korea. I joined to work on business development as well as sales of this system.

We sold some, but the customer feedback was not great. Customers were experiencing greater issues in their network than normal and tunable transceivers costs were way too high. While searching for a better solution, we came accross two scientists at a university in Montreal. They were working on a unique technology with applications in the medical space. We conducted some proof of concepts to see if this could fit in the telecom and datacom world. The answer was yes, causing us to jump head first into the silicon photonic world.

Q. WHY ARE YOU PART OF SILICON CATALYST?

A. The first time that I heard about Silicon Catalyst, I thought it was a VC. I saw first the access to investment, then the in-kind services.



Philippe Babin CEO AEPONYX

I was seeing Silicon Catalyst as an accelerator that would provide us visibility and value by virtue of its in-kind services. Initially, we found it tough to put a value on. Our development plan was not that definitive and the fit with MEMS technologies was not clear. We are not pure silicon which meant it was difficult to do the math and calculate whether it was worth us giving up equity?' It was difficult to make the call. Short term we were preoccupied with financing yet were seeing some intriguing doors being opened to VCs.

A CONVERSATION WITH AEPONYX CEO PHILIPPE BABIN ... CONTINUED



Q. WHAT INSPIRED YOU TO START THE COMPANY? A. I MUST ADMIT THAT I GOT DRAGGED INTO IT.

Q. WHAT HAVE YOU LEARNED IN THE PAST YEAR?

A. We learned so much that it is hard to know where to start. First, we validated our market and product specification. Since we filed our 6 patent applications 18 months ago, we have been spending time with our customers with the aim of figuring out exactly what was needed, which helped us to refine the product specifications of our first two commercial products last year. It also helped us to clearly identify and target our first customers. Second, we learned how to work with Silicon Catalyst, which one must admit is a bit of a start-up too. But the great thing that happened with Silicon Catalyst last year is they understood our needs, adapted their in-kind portfolio, adding new relevant partners like Ansys, Solidworks, IMT, SoftMEMS and Lumerical (who has provided us with a fundamental tool to innovate with Silicon Nitride Photonics). They also supported our specific efforts with national grants. Thanks to this agreement, we closed our \$1.9 Million grant with Sustainable Development Technology Canada. The recognition from Silicon Catalyst has also opened up several doors during our seed round.

Q. WHAT ARE YOUR GOALS FOR 2017?

A. This year is a very exciting one for us and includes many milestones. 2017 will be the year of our first proto-

type demonstration, our first customer agreement and furthermore, will be the year in which we close on our seed financing. We are now in the closing stages so stay tuned for an important upcoming announcement.

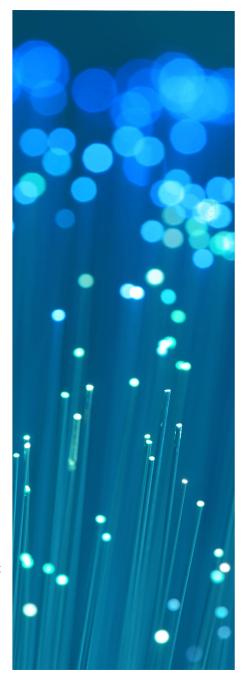
Q. WHAT RECOMMENDATIONS DO YOU HAVE FOR SEMICONDUCTOR ENTREPRENEURS?

A. The semiconductor space requires patience. An entrepreneur should leverage what is available and use non dilutive tools, use non dilutive tools, use shared infrastructures and make sure to communicate with your customer as soon as you have your IP protected. Identifying what is needed then focusing on collaboration is key.

Q. WHAT IS IT LIKE AS AN INTER-NATIONAL COMPANY WORKING WITH SILICON VALLEY?

A. We are located in Montreal, QC, Canada. I go to the Valley once every 5-6 weeks to meet with partners and customers. Having a presence there is very useful. I spent most of my career traveling for business development so distance has never been an obstacle.

Working remotely with Silicon Catalyst has been easy and there have been no issues. There are already 3 Silicon Catalyst start-ups in Montreal, and I feel this will grow in time. We know each other and we will help each other to succeed.







06.18-22.2017 **DAC 2017**

Austin Convention Center, Austin, TX

DAC is the premier conference and trade show devoted to the design and automation of electronic systems (EDA), embedded systems and software (ESS), intellectual property (IP), Security, IoT, and Automotive applications. Click here to register.

6.21.2017 **SEMICONDUCTOR VENTURE CAPITAL PANEL**

Google Developers Launchpad space. San Francisco

Presented by Silicon Catalyst and hosted at Google Developers Launchpad, a mentorship-based global startup program.

What does the future hold for semiconductor industry startups?

Click here to register.

07.10.2017 **IMEC TECHNOLOGY FORUM**

San Francisco Marriott Marquis

Imec Technology Forum USA is imec's high-tech event, offering a unique research and innovation perspective on the emerging opportunities in semiconductor technology and smart electronics systems.

Click here to register.

07.11-13.2017 **SEMICON WEST**

Moscone Center, San Francisco, CA

SEMICON West is the premier annual event for the global microelectronics industry, highlighting the latest innovations, products, processes, and services for the design and manufacture of today's most sophisticated electronics.

Click here to register.

08.20-22.2017 **HOT CHIPS 29**

Flint Center for the Performing Arts, Cupertino, CA

Sessions covering GPU's and Gaming, IOT/Embedded, Automotive, Processors, FPGAs, Neural Nets, Architecture and Servers

Click here to register.

09.27.2017 **GSA ENTREPRENEURSHIP** CONFERENCE

Leuven, Belgium

Our speakers will share their personal experiences in financing, IPO and acquisition exits for startups, as well as discussing the most promising areas of innovation in the European and global semiconductor ecosystem. Click here to register.

11.05-08.2017 **ITPC 2017**

Fairmont Orchid, Big Island, Hawaii

ITPC-brings together top executives and technology strategists from the world's leading technology companies to explore the trends driving business in microelectronics and the devices and applications they enable. Click here to register.

11.14-17.2017 **SEMI EUROPA**

Munich, Germany

The Largest Microelectronics Event in Europe For the first time co-located with productronica in Munich, Germany creating the strongest single event for electronics manufacturing in Europe, and broadening the range of attendees across the electronics supply chain.

Click here for more information.

12.07.2017 **GSA AWARDS DINNER CELEBRATION**

Santa Clara, California

The industry's premier event celebrating the achievements of semiconductor companies in several categories ranging from outstanding leadership to financial accomplishments as well as overall respect within the industry.

Click here to register.



SILICON CATALYST IN-KIND PARTNER PROFILE



Lumerical develops photonic simulation software - tools which enable product designers to understand light, and predict how it behaves within complex structures, circuits, and systems.



Bill De Vries Director of Marketing at Lumerical

Started in 2003, in an apartment solarium overlooking downtown Vancouver, Lumerical has grown to over 50 employees, with customers in more than 40 countries across the world. From inception, the company founders believed that semiconductors would be increasingly influential on the world we live in, and that semiconductor manufacturing processes could provide a scalable, low cost platform for photonics – the science of light and its interaction with matter – to unlock possibilities in diverse applications including: biotech-

nology, communications, information storage, solar energy, environmental sensing,

and consumer electronics. Over many days and nights in the solarium, and subsequently in three different office sites in downtown Vancouver, Lumerical has worked to engineer software tools that allow scientists and engineers to create, test, and optimize the photonic technologies of tomorrow.

Initially, Lumerical focused on developing best-in-class 3D physical design tools for applications such as CMOS image sensors, plasmonics, LCD displays, microwire polarizers, and metamaterials. Through consistent investment back into product R&D and a close working relationship with our customers, Lumerical was able to outpace incumbent vendors in the field, and establish itself as a recognized leader in photonic simulation software. Fueled by requests from our customers, and as the semiconductor ecosystem began to show technical and commercial promise with integrated photonics, Lumerical began an initiative to develop a suite of tools specifically targeted at

enabling the design and analysis of photonic integrated circuits (PICs), which are microchips capable of carrying light instead of electricity

The opportunity for Lumerical in integrated photonics stemmed from the established IC design and layout tools being heavily optimized for traditional electronic IC design. It was necessary for early innovators in integrated photonics to be specialized and vertically integrated. Hiring and developing PhD-level expertise up and down the semiconductor ecosystem, from process engineering, to component and circuit design, was necessary for even basic research activities in the field. Design tools were either non-existent or specialized for a specific segment of the flow. Companies and researchers developed custom design methodologies, piecing together various tools, often involving many manual steps, reminiscent of the very early days of electronic IC design. In working with our customers, Lumerical recognized the opportunity to develop software tools that enable a scalable photonic design ecosystem complementary to electronic IC design.

SILICON CATALYST IN-KIND PARTNER PROFILE



Lumerical develops photonic simulation software - tools which enable product designers to understand light, and predict how it behaves within complex structures, circuits, and systems.

Critical to this offering is abstracted, large scale "spice-like" photonic circuit design and simulation, a robust process design kit (PDK), and simulation model development workflows. With these tools and capabilities, the requirement for vertically integrated expertise lessens. Fabless-style business models become viable where semiconductor foundries can focus on developing reliable components and processes that technology integrators and developers can use to design innovative new applications, such as complex modulators for high speed optical communications, or very low power integrated optical transceivers for the explosion of capacity in modern data center infrastructure.

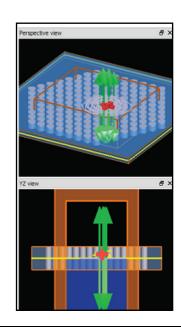
The role of startups and Silicon Catalyst

Much of the innovation in integrated photonics has come from academic research and the startup ecosystem.

There are many examples of large organizations acquiring integrated photonics startup companies. Cisco acquired Lightwire in 2012, Huawei acquired Caliopa in 2013, Juniper Networks acquired Aurrion in 2016, to name a few. With most of the technology investment dollars going to software and service organizations in recent years, semiconductor innovation at the startup level has all but disappeared. Tapeout and design tool costs are non-trivial and failure of first-time right silicon can mean the end of a venture. Given these challenges, Lumerical still believes there is opportunity within the semiconductor ecosystem, and we're excited to work with Silicon Catalyst to enable a regenesis of startup innovation in semiconductors. Making our tools and expertise available to the innovative and inspiring companies working with Silicon Catalyst gives us the ability to continue to be at the forefront of photonic technology, to assist in bringing technology ideas to market, and to continue to evolve our products to best leverage innovations in semiconductor

processes and enable fast, accurate photonic design.

Lumerical joined Silicon Catalyst as an in-kind partner in March, 2017 and we currently collaborate with two companies in Silicon Catalyst's portfolio. We hope to continue to learn from the diverse and innovative organizations within Silicon Catalyst's portfolio and we look forward to the opportunity to enable other portfolio companies to maximize their opportunity and success.





PRESS RELEASE **APRIL 4, 2017**



SPARK MICROSYSTEMS JOINS THE SILICON CATALYST INCUBATOR

Montreal, Canada and Silicon Valley, California

Silicon Catalyst, the world's only startup incubator focused exclusively on semiconductor solutions, based in Silicon Valley, CA, announced the admission of the startup SPARK Microsystems into the incubator.

SPARK Microsystems offers a unique and innovative wireless transceiver technology that achieves an order of magnitude better energy efficiency and latency than Bluetooth Low Energy or ZigBee at faster data rates. The technology significantly extends the battery life of electronics, and enables the battery-less operation of wireless devices such as sensors when paired with energy harvesting technologies.

Silicon Catalyst CEO Rick Lazansky said, "By enabling wireless to become the most energy efficient part of a system, SPARK Microsystems is poised to disrupt the perception of the wireless function by system engineers."

Key market drivers for SPARK Microsystems are notably related to the growth of the Internet of Things, which includes high volume segments such as sensor networks, home and industrial automation.

smart buildings, automotive, medical wireless devices and consumer electronics.

SPARK Microsystems Cofounder Frederic Nabki explains, "Businesses looking to enter the Internet of Things with battery or energy harvesting operated devices face a critical challenge: there is no suitable wireless solution available to provide satisfactory battery life without significantly compromising link performance or responsiveness. Our technology changes that. Thanks to the in-kind partners provided by Silicon Catalyst and to their connections to expert advisors, we will significantly reduce the timeto-market of our innovative wireless technology. We are excited to be part of the semiconductor development and investment in Canada, in cooperation with Silicon Catalyst."

John East, former CEO of Actel said. "No one wants an array of IoT devices that must have their batteries changed often. In fact, no one wants to change batteries at all. Energy harvesting is the answer, but with the upcoming requirements for transferring larger amounts of data at ever-increasing rates, today's 'standard' wireless techniques are more or less incompatible with energy harvesting. Today's standards were created to solve a specific set of requirements and they did it well, but times are

changing. Today's standards won't solve tomorrow's problems. In fact, they have become tomorrow's problem! SPARK is the answer." East joined SPARK Microsystems as a Senior Advisor and Strategist after being introduced to its founders and technology by Silicon Catalyst.

About SPARK Microsystems

SPARK Microsystems is a fabless semiconductor company that is leading the way towards ultra-low power wireless communications for the Internet of Things revolution. With its patented technologies, SPARK Microsystems will bring to market a high performance wireless transceiver that allows for orders of magnitude improved power consumption and latency while providing higher data rates than competing technologies. For more information, please visit www.sparkmicro.com.





PRESS RELEASE **MARCH 7, 2017**



TECHCODE AND SILICON CATALYST COMBINED STRENGTH CREATES UNPRECEDENTED GLOBAL SUPPORT NETWORK FOR SILICON-FOCUSED START-UPS

San Francisco, California

TechCode and Silicon Catalyst Collaboration to Rev Up **Semiconductor Start-Up Ecosystem**

Incubators combined strength creates unprecedented global support network for silicon-focused start-ups

TechCode, a global network of startup incubators and entrepreneur ecosystems, and Silicon Catalyst, the world's leading incubator focused on silicon and electronics systems, today announces a partnership to support startups set to deliver innovative silicon and electronics systems. Leveraging the unique strengths of each program. startups from the TechCode cohort can utilize the Silicon Catalyst extensive semiconductor ecosystem to develop products in a variety of industries including healthcare, robotics, artificial intelligence and more, while Silicon Catalyst startups benefit from Tech-Code locations and investor network.

"As TechCode's startups today increasingly focus on innovations that require tapping into the semiconductor ecosystem, Silicon Catalyst will provide a critical element vital to the success of these startups," said TechCode CEO Erica Huang. "No one has built a better position in this ecosystem than Silicon Catalyst and we're thrilled to add them to our growing list of bestin-class partners in an effort to further assist the startups in our global accelerator programs."

With an extensive global network of incubators across locations such as Silicon Valley, Beijing, Shanghai, Shenzhen, Seoul, Berlin, Finland and Tel Aviv, TechCode is uniquely capable of helping startups scale globally. To help startups enter the China market, TechCode has a strategic partnership with CFLD, a leader in investment and operation of new industrial cities. CFLD integrates industry, university, research, government and finance resources in over 40 industrial new cities across the world.

Headquartered in Silicon Valley and servicing startups globally, Silicon Catalyst also offers silicon-focused startups opportunities to partner with leading industry players such as TSMC, Synopsys and Texas Instruments to provide guidance and connections throughout the semiconductor ecosystem. With the focus on developing an ecosystem that makes it feasible for semiconductor and electronic systems startups to thrive, the TechCode and Silicon Catalyst partnership will further allow participating startups to spur innovation in the semiconductor industry.

"TechCode's extensive global reach will allow us to work with entrepreneurs around the world as the startup ecosystem rapidly expands beyond Silicon Valley," said Silicon Catalyst

CEO Rick Lazansky. "The TechCode worldwide presence provides an opportunity for global incubation at any of the TechCode's international locations for Silicon Catalyst's startups."

To learn more about TechCode, visit http://us.techcode.com/ and to learn more about Silicon Catalyst, visit http://siliconcatalyst.com/.

About TechCode

Focusing on incubator operation management and technology startup cultivation, TechCode is committed to building the world's leading entrepreneurship ecosystem. Integrating a wide range of global resources, Tech-Code has established incubators in Beijing, Shanghai, Shenzhen, Gu'an, Silicon Valley, Seoul, Finland, Tel Aviv and Berlin to help get its startup members on their feet and grow rapidly.

About CFLD

CFLD (China Fortune Land Development Co, Ltd) integrates five resources: industry, university, research, government and finance in over 40 industrial new cities across the world. Depending on the layout of its business partner they pioneer an innovative technology chain including incubation-acceleration-industrialization-commercialization, providing full support to entrepreneurs from the "first step" to "every step" of entrepreneurship to the last mile.

PRESS RELEASE FEBRUARY 3, 2017

TECHCO

TECH.CO ON ACCELERATORS TO WATCH FOR IN 2017

Conor Cawley - When it comes to success in the business world, startups get all the glory. Whether their innovative products have transformed a struggling industry or their fascinating new software has created a new means of doing business, startups are lorded as the heroes of innovation. But there's a lot to be said about another institution that plays a big roll in the success of these burgeoning companies; an unsung hero that goes unappreciated when unicorns rise and fall: accelerators.

Accelerators are the ones that support and nurture startups from an early-stage. In addition to providing funding, they often house these startups in beautiful buildings with enough networking opportunities and mentoring experience to fuel their success in the long run.

With all the trends for 2017 getting set in stone, it's important to know which accelerators are on the way up in the new year. Take a look at a few accelerators that are poised to make some noise in 2017:

SILICON CATALYST (SEMICONDUC-TOR SOLUTIONS)

Based out of San Jose, Silicon Catalyst is the world's first incubator focused exclusively on semiconductor solution startups. A recipient of the prestigious UBM Canon Startup Company of the Year in 2015, Silicon Catalyst selects startups to incubate in conjunction with an ecosystem of in-kind partners, industry-leading companies, and potential investors.

2.

TECHCODE GLOBAL AI+ ACCELERA-TOR (ARTIFICIAL INTELLIGENCE)

Based out of Mountain View, California, TechCode's accelerator helps startups from locations all over the world, rather than limiting them to one city. Additionally, the program has a unique focus on the applications of artificial intelligence technologies in a variety of industry verticals.

(Connor Cawley is a Senior Writer for Tech.Co)



A VALUABLE RESOURCE FOR THE SEMICONDUCTOR STARTUP COMMUNITY









VOLUME 1 **JUNE 2017**