<table>
<thead>
<tr>
<th>REQUISITION NUMBER:</th>
<th>2022-13</th>
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<tbody>
<tr>
<td>MRDA POSITION TITLE:</td>
<td>Electronics Engineer</td>
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<tr>
<td>REVISION DATE:</td>
<td>June 2022</td>
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<tr>
<td>REPORTS TO:</td>
<td>Electronics and Integrated Circuits Manager</td>
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<tr>
<td>LOCATION:</td>
<td>Kirkland, WA</td>
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<tr>
<td>STATUS:</td>
<td>Exempt/Salaried; Full-time</td>
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<tr>
<td>TRAVEL:</td>
<td>Occasional business travel; domestic and international (based upon travel restrictions and overall conditions)</td>
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<tr>
<td>EDUCATION REQUIRED:</td>
<td>BS in Electrical Engineering or related field</td>
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<td>REQUIRED EXPERIENCE:</td>
<td>3+ years of industry experience or Master of Science in Electrical Engineering (6+ years of industry experience preferred)</td>
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| REQUIRED SKILLS: | • Perform board level, mixed signal circuit design and simulation, typically at 5V and below.  
• Integrating FPGA, microcontroller and System on a Chip components into designs.  
• Ability to debug hardware as needed using diagnostic tools such as oscilloscopes and logic analyzers.  
• A willingness to continually learn new skills and concepts, with an ability to handle new and varied work situations with moderate complexity.  
• Have a passion for collaboratively finding ways to make designs and products better.  
• Great written and oral communication skills, including the ability to document design concepts in detail and clearly explain technology and concepts to non-specialists.  
• Work effectively either independently or in a cross-functional team.  
• Demonstrate initiative and motivation. |
| BENEFICIAL EXPERIENCE/SKILLS: | • System level design.  
• Familiarity with FPGA, microprocessor, and/or DSP architectures.  
• FPGA programming in Verilog or System Verilog.  
• Embedded firmware programming in C/C++.  
• Knowledge of dimensional metrology devices and measurement techniques.  
• Understanding of sensor design and technology. |
- Knowledge of Japanese language, culture, and business practices.
- Beneficial application knowledge: Altium, PSpice, FPGA toolsets, ModelSim/Questa, LabVIEW, Visual Studio and other IDE's, Team Foundation Server/Azure DevOps, Visio and RedHat Linux.
- Other beneficial programming languages: C#, assembly, LabVIEW, and Python.

**DUTIES & RESPONSIBILITIES:**
- A primary focus on designing, debugging, testing and documenting discrete board-level mixed signal circuits and/or FPGA implementations as part of a larger project. Interfacing with the PCB Engineer to guide layout as needed.
- Interfacing with other project team members of different disciplines to ensure achieving the requirements of the design.
- Writing detailed documentation including, but not limited to, specifications, presentations, test result reports, and research reports.
- Occasionally running test systems and analyzing results as needed, in the context of the project.
- Actively participating in design reviews with fellow team members.
- Participating in occasional late afternoon net meetings with our colleagues in Japan.
- Attending occasional web meetings outside of regular working hours.

**ADDITIONAL INFORMATION:**
MRDA offers a flexible, challenging, casual team-oriented work environment, a hybrid work model, competitive salaries and an excellent benefit package, including matching 401k.

Please apply for this position [here](#).

Please visit [www.mitutoyo-rda.com](http://www.mitutoyo-rda.com) for additional information. Applicants must be authorized to work for any employer in the U.S.

*Disclaimer:* The above information on this description has been designed to indicate the general nature and level of work performed by Team Members within this classification. It is not designed to contain or be interpreted as a comprehensive inventory of all duties, responsibilities, and qualifications required of Team Members assigned to this job.