For Immediate Release

TransSiP's JC-PFM™ DC-DC Conversion Solution and Harmony™ SNJ Conditioner named 2016 ECN IMPACT Award Finalist

Irvine, California, August 29, 2016- ECN/Electronic Component News announced today that TransSiP's submission to the 2016 ECN IMPACT Awards has been named a 2016 ECN IMPACT Awards Finalist.

The ECN IMPACT Awards recognize the top products and services in 17 categories across the design engineering landscape. The competition seeks to honor ingenuity and creativity among companies who are making a difference in the industry and in the lives of engineers around the globe.

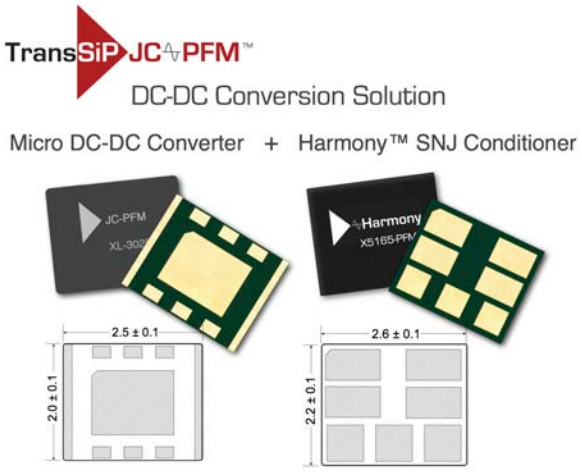
TransSiP's IMPACT Awards Finalist is a synchronous step-down micro DC-DC converter chipset integrating switching controller, inductor, and TransSiP's patent-pending JC™ switching noise jitter ("SNJ") conditioning technology. JC™ SNJ conditioning addresses the critical problem of chaotic noise present on the regulated output voltage of pulse-frequency modulation (PFM) type switching DC-DC converters. Up to now this problem has excluded PFM DC-DC converters from use with applications sensitive to supply bias noise, particularly in portable/wearable, remote and IoT devices incorporating spread-spectrum wireless communications and navigation/positioning. TransSiP's JC™ technology conditions SNJ on the output of the JC-PFM™ DC-DC converter chipset providing a noise-optimized supply bias to wireless SoCs, RF components, high precision TCXOs, ADCs and other noise sensitive RF and analog circuit elements.

This development was made possible by an innovative application of real-time spectral histogram analysis using the DPX® technology from Tektronix. This technique enabled the identification of switching noise jitter as a dominant factor in chaotic noise problems of switched mode DC-DC converters and led to the systematic development of a new class of function- the switching noise jitter conditioner. Marketed as TransSiP Harmony™ and JC-PFM™ DC-DC solutions, this technology will enable the use of highly efficient PFM DC-DC conversion with the noise-sensitive circuitry at the heart of portable/wearable/IoT and remote devices

with no compromise necessary to achieve maximum systems performance in all conditions. This will lead not only to enhanced user experience, but also significant improvements in battery life and consequential impact on the overall terrestrial energy budget.

Field tests of GPS/GNSS systems under limiting conditions have shown circuit performance powered by TransSiP's JC-PFM™ DC-DC converter to be equivalent if not superior to circuits powered by the low noise linear ("LDO") regulators employed in practically all power-constrained applications today. In addition to an improved supply bias noise signature, the JC-PFM™ DC-DC converter provides highly efficient (80 – 95%) power conversion from a battery to the downstream systems, whether operating in power saving modes or at full load. Compared to 5% – 60% for LDO, power-constrained systems using TransSiP's JC-PFM™ conversion or JC™ conditioning technology will have more autonomy- a lot more, as much as 5X- and the whole sleep/standby/full power paradigm used by the portable/wearable industry can be revisited to optimize user experience.

This first release of TransSiP's JC-PFM™ DC-DC converter consists of a synchronous step-down micro DC-DC converter using PFM control in a 2 x 2.5mm LGA package, and the Harmony™ SNJ conditioner module in a 2.2 x 2.6mm plastic LGA package, both 1mm in height. This chipset configuration gives the system designer maximum flexibility in layout, and the Harmony™ SiP can be sourced individually for use as a standalone SNJ conditioner. A second release will combine the DC-DC converter IC, inductor, and JC™ circuitry in an LGA or BGA microSiP module measuring 3.7 x 2.9mm or less.



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